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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/600,916	06/20/2003	Florian Schamberger	P2000,0342	6913
24131 75	590 05/13/2004		EXAMINER LAM, TUAN THIEU	
LERNER AN P O BOX 2480	D GREENBERG, PA			
	D, FL 33022-2480		ART UNIT	PAPER NUMBER
			2816	
	•		DATE MAILED: 05/13/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)				
	Office Action Summary	10/600,916	SCHAMBERGER, I	FLORIAN			
	Cinoc Action Summary	Examiner	Art Unit				
- }	The MAIL INC DATE And	Tuan T. Lam	2816				
	The MAILING DATE of this communication appears on the cover sheet with the correspondence address						
	A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period we Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	IS SET TO EXPIRE 3 No se(a). In no event, however, may a within the statutory minimum of thir ill apply and will expire SIX (6) MON	IONTH(S) FROM reply be timely filed by (30) days will be considered timely. ITHS from the mailing date of this com-				
	Status						
	1) Responsive to communication(s) filed on 03 Oc	tober 2003					
1		action is non-final.					
	3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is						
	closed in accordance with the practice under Ex	(parte Quavle, 1935 C.D		nerits is			
	Disposition of Claims	7	. 11, 1 33 O.G. 213.				
1		b					
	4) Claim(s) <u>1-10</u> is/are pending in the application.						
	4a) Of the above claim(s) is/are withdraw	n from consideration.		•			
	5) Claim(s) is/are allowed.						
-	6)⊠ Claim(s) <u>1-10</u> is/are rejected.			•			
	7) Claim(s) is/are objected to.		•	·			
-	8) Claim(s) are subject to restriction and/or	election requirement.					
1	Application Papers			, •			
	9) The specification is objected to by the Examiner.		•				
	10) The drawing(s) filed on 20 June 2003 is/are: a)	accontact or by attack					
	Applicant may not request that any objection to the dr	and accepted of D) object	ted to by the Examiner.				
	Replacement drawing sheet(s) including the correction	n is required if the drawing/	e. See 37 CFR 1.85(a).				
	11) The oath or declaration is objected to by the Exar	niner. Note the attached	Office Action or form DTO	1.121(d).			
P	riority under 35 U.S.C. § 119	and and and	Office Action of John P 10-	102.			
-							
	12) Acknowledgment is made of a claim for foreign pr	riority under 35 U.S.C. §	119(a)-(d) or (f).	*			
	a) △ All b) ☐ Some ^ c) ☐ None of:	•					
	- Priority documents i	nave been received.		**			
	Prior of the priority documents i	nave been received in Ap	olication No				
	— 1 1 2 3 mile defined depices of the pitolity	documents have been re	eceived in this National Sta	ge			
	application from the International Bureau (I	201 Rule 17.2(a)).	an an apres of the control of the second				
	* See the attached detailed Office action for a list of	une certified copies not re	eceived.				
At	tachment(s)	,					
	Notice of References Cited (PTO-892)	Λ.Π					
2)	☐ Notice of Draftsperson's Patent Drawing Review (PTO 048)	4) LJ Interview Sur Paper No(s)/I	nmary (PTO-413) Mail Date				
	Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 8/5/03, 6/20/03.	5) Notice of Info	rmal Patent Application (PTO-152	2)			
PΤΟ	L-326 (Rev. 1-04)						

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DETAILED ACTION

Claim Rejections - 35 USC § 112

- 1. The following is a quotation of the second paragraph of 35 U.S.C. 112:
 - The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
- 2. Claim 1-10 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claim 1, the recitation of "can be" is not a positive recitation. It is suggested to change "can be" to --is--.

Claims 2-10 are indefinite because of the technical deficiencies of claim 1.

Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1, 3, 4, 6, 7 and 10 are rejected under 35USC 103(a) as being unpatentable over Foss et al. (USP 5,245,576) in view of Hwang et al. (USP 5,777,491). Figure 4 of Foss et al. shows a driver circuit comprising an input stage (20, 21) having input terminals for receiving first and second input signals (Aij, Akl), an output stage (27, 28, 29) connected to said input stage having a terminal (source of transistors 28, 29) for feeding a blowing voltage (Vpp), a first supply terminal (ground), an output terminal (node 24) coupled to the link (23) and the output signal with the boosted signal level (Vpp) is tapped at said output terminal.

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et al.

The differences seen between Foss et al. and the present invention is that Foss et al. does not show the detailed structure of the input stage comprising logic gates (20, 21) having first and second pair of switches for receiving first and second input signals as called for in claims 1 and 10. Figure 1 of Hwang shows the detailed structure of logic gates (11, 12) performing AND/NAND logical operation in simplest form of having only four transistors. This configuration occupies little space on a chip and consumes a minimal amount of power. Therefore, it would have been obvious to a person skilled in the art at the time the invention was made to implement the logic gates of Foss et al. with the arrangement as taught by Hwang et al. for the purpose of saving power consumption on a chip.

Regarding claim 3, first switch pair is seen transistors N4 and N1 of Hwang et al.

Regarding claim 4, cross-coupled transistors are seen as the transistors 28 and 29 of Foss

Regarding claim 6, second switch pair of transistors are seen as transistors N2 and N3 of Hwang et al.

Regarding claim 7, first and fourth transistors are seen as transistors (N3 and N4) connected in parallel, and second and third transistors are seen as transistors N1 and N2 of Hwang et al.

Allowable Subject Matter

4. Claims 2, 5 and 8-9 would be allowable if rewritten to overcome the rejection(s) under 35 U.S.C. 112, second paragraph, set forth in this Office action and to include all of the limitations of the base claim and any intervening claims.

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Conclusion

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. In this regard, applicant's cited prior art has been carefully considered.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan T. Lam whose telephone number is 571-272-1744. The examiner can normally be reached on Monday to Friday (7:30 am to 6:00pm).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, TIMOTHY P CALLAHAN can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tuan T. Lam Primary Examiner Art Unit 2816

5/9/2004